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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10054540	01/22/2002	438	200 ^F	2812	H P:AM

**APPLICANTS: Kim Hyun-Ho; Kim Ki-Nam;

**CONTINUING DATA VERIFIED: H?

None

** FOREIGN APPLICATIONS VERIFIED: H?

REPUBLIC OF KOREA 2001-5147 02/02/2001

YES

PG-PUB DO NOT PUBLISH ☐

RESCIND ☐

Foreign priority claimed ☒ yes ☐ no

35 USC 119 conditions met ☒ yes ☐ no

Verified and Acknowledged Examiners's initials H?

ATTORNEY DOCKET NO

5649-926

TITLE : Integrated circuit ferroelectric memory devices including plate lines directly on ferroelectric capacitors and methods of fabricating the same

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED	
			Total Claims	Print Claim for O.G.
ISSUE FEE		Primary Examiner	DRAWING	
Amount Due	Date Paid		Sheets Drwg.	Figs.Drwg.
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	Application Examiner	
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